



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re U.S. Patent Application of )  
ITO )  
Application Number: 10/770,479 )  
Filed: February 4, 2004 )  
For: INTEGRATED CIRCUIT, INTEGRATED )  
CIRCUIT DESIGN METHOD AND )  
HARDWARE DESCRIPTION GENERATION )  
METHOD TO GENERATE HARDWARE )  
BEHAVIOR DESCRIPTION OF INTEGRATED )  
CIRCUIT )  
Attorney Docket No. NITT.0189 )  
)

**Art Unit 2825**

**Examiner  
Thompson, Annette M.**

**Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231**

**PETITION FOR EXTENSION OF TIME**

Sir:

Applicants request that the time for taking action in this case be extended pursuant to 37 C.F.R. § 1.136(a) for **one (1) month**. The fee set in 37 C.F.R. § 1.17 for the extension of time is **\$120.00**.

Please charge any additional fee required for this extension of time to Deposit Account 08-1480. A duplicate copy of this paper is enclosed.

Also filed is *a Response to an Office Action*.

Respectfully submitted,

02/08/2005 MGEBREM1 00000156 10770479

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February 4, 2005